

FIG. 1

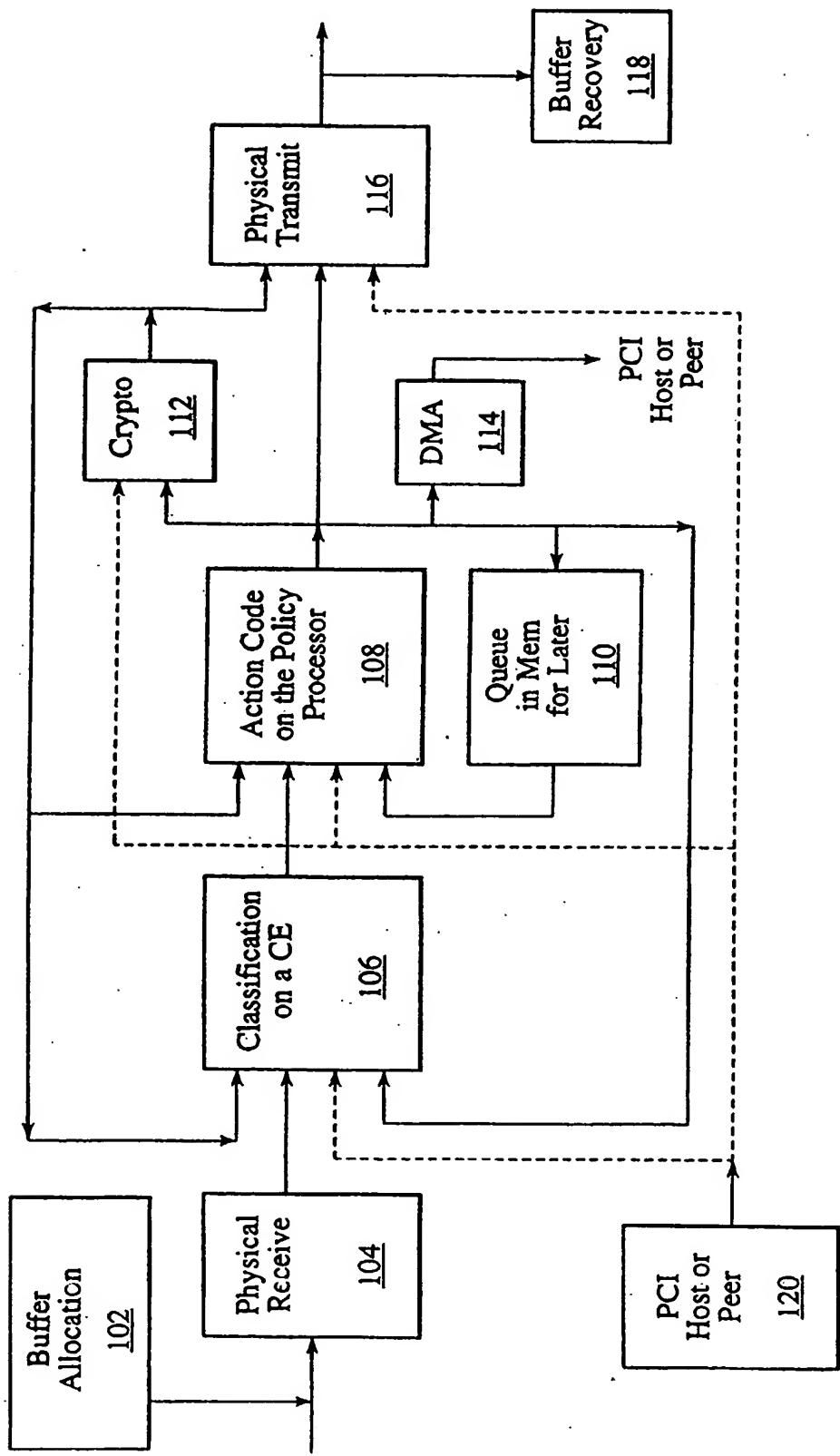


FIG. 2

图 3 网卡内部结构示意图

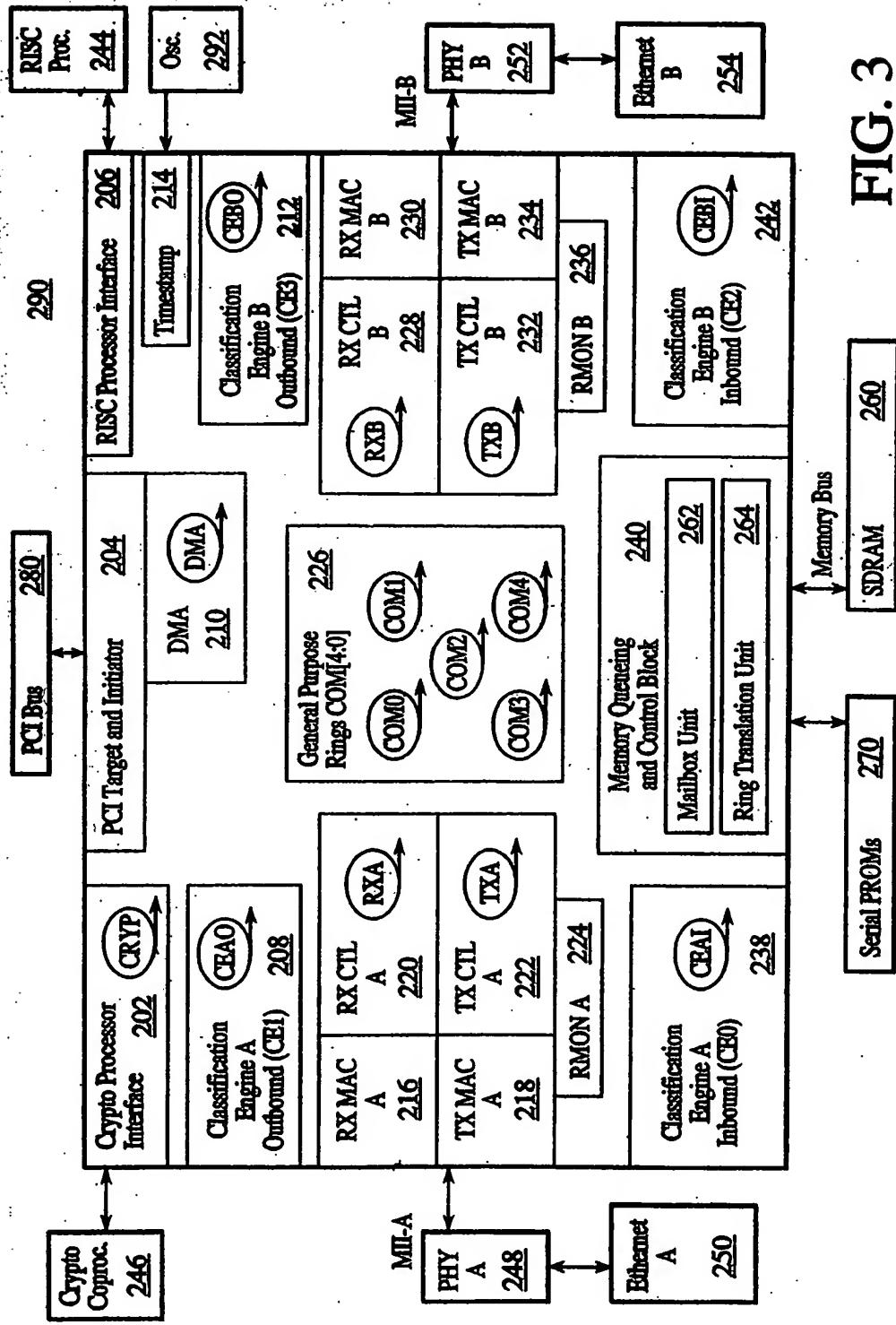


FIG. 3

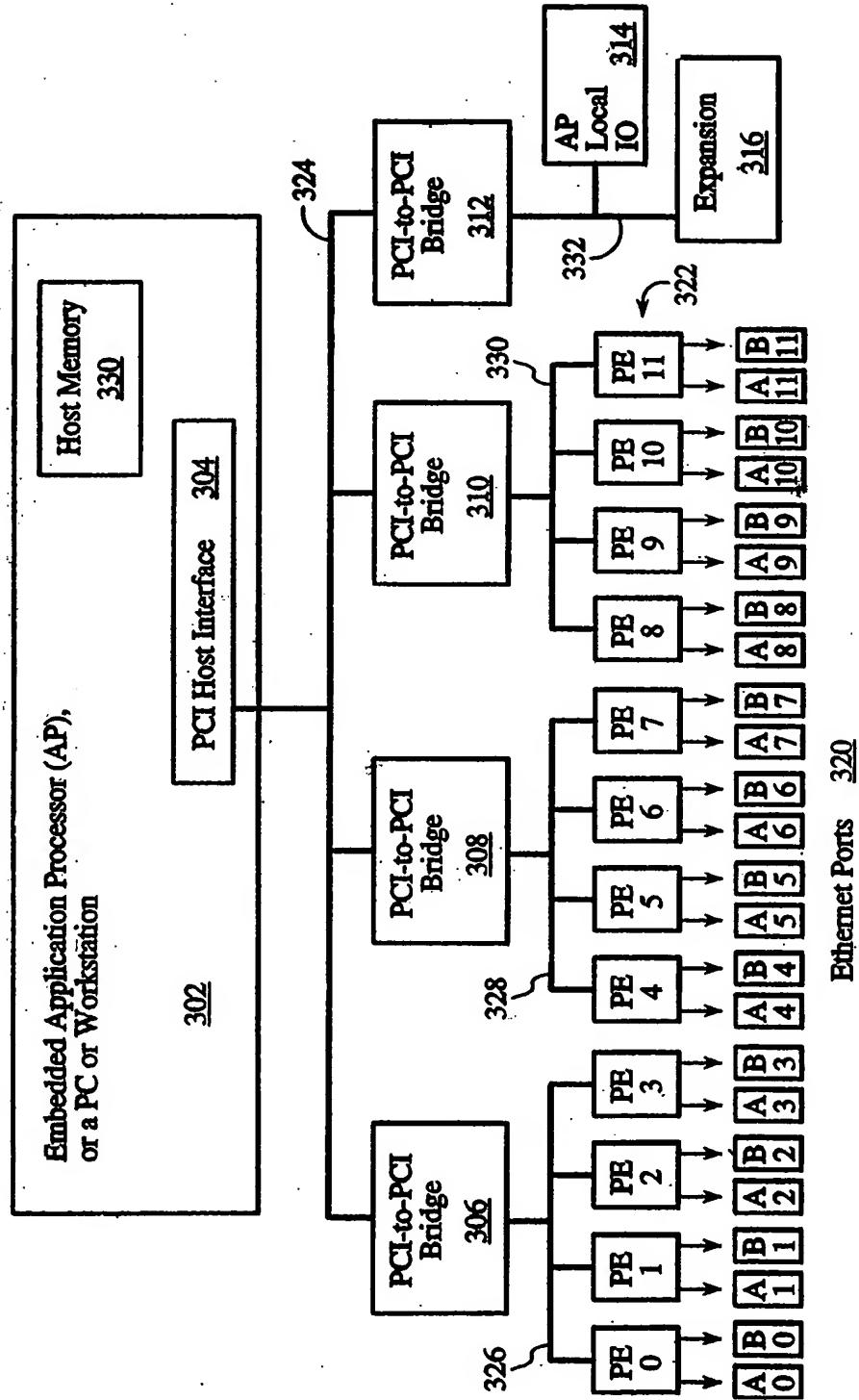


FIG. 4

<u>Ring Base Register 400</u>	<u>THRESHOLD REPORTED</u>
+ 0x0000	RX_A Ring <u>402</u>
+ 0x1000	RX_B Ring <u>404</u>
+ 0x2000	TX_A Ring <u>406</u>
+ 0x3000	TX_B Ring <u>408</u>
+ 0x4000	Reclassify_A_Inbound Ring <u>410</u>
+ 0x5000	Reclassify_A_Outbound Ring <u>412</u>
+ 0x6000	Reclassify_B_Inbound Ring <u>414</u>
+ 0x7000	Reclassify_B_Outbound Ring <u>416</u>
+ 0x8000	DMA Ring <u>418</u>
+ 0x9000	Crypto Ring <u>420</u>
+ 0xA000	COM0 Ring <u>422</u>
+ 0xB000	COM1 Ring <u>424</u>
+ 0xC000	COM2 Ring <u>426</u>
+ 0xD000	COM3 Ring <u>428</u>
+ 0xE000	COM4 Ring <u>430</u>

{ 440 }

The 5 General Purpose Rings have Prog:
 <256-empty/>256-full Threshold as set in
 the RBASE Register.

FIG. 5

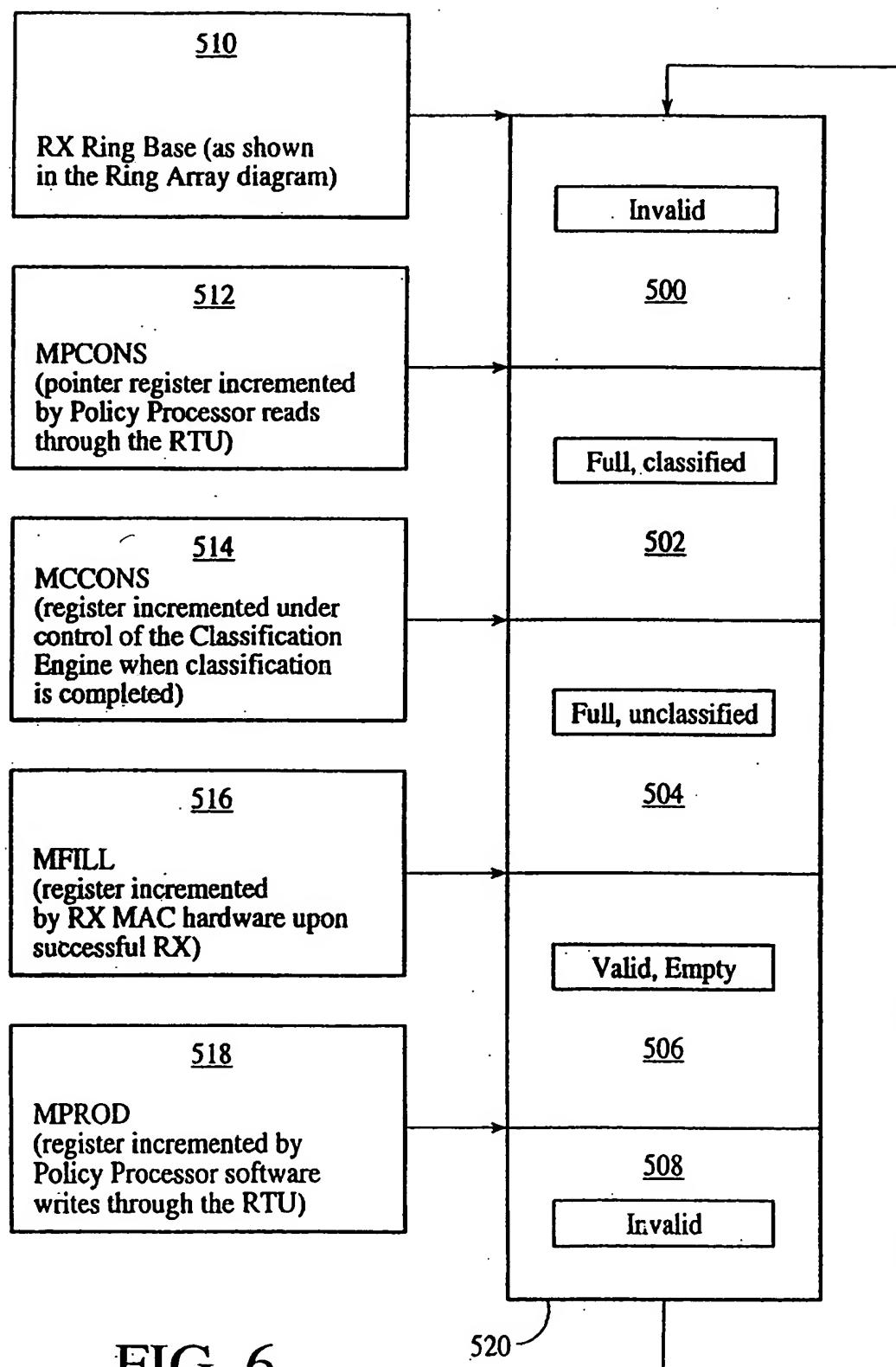


FIG. 6

#

Buffer offset: Word 0x0

	RX Status Word	<u>600</u>	Byte 0x0
0x1	RX Timestamp	<u>602</u>	0x4
0x2	TX Status Word	<u>604</u>	0x8
0x3	TX Timestamp	<u>606</u>	0xC
0x4		<u>608</u>	0x10
	Pad space (112 bytes)		
0x20	0x0000	<u>610</u>	0x80
618	Ether Header (14 bytes)		
	Payload (up to 1500 bytes) plus Ethernet CRC		
		<u>612</u>	
	Available for software use		
Word 0x200		<u>614</u>	Byte 0x800
	620		

FIG. 7

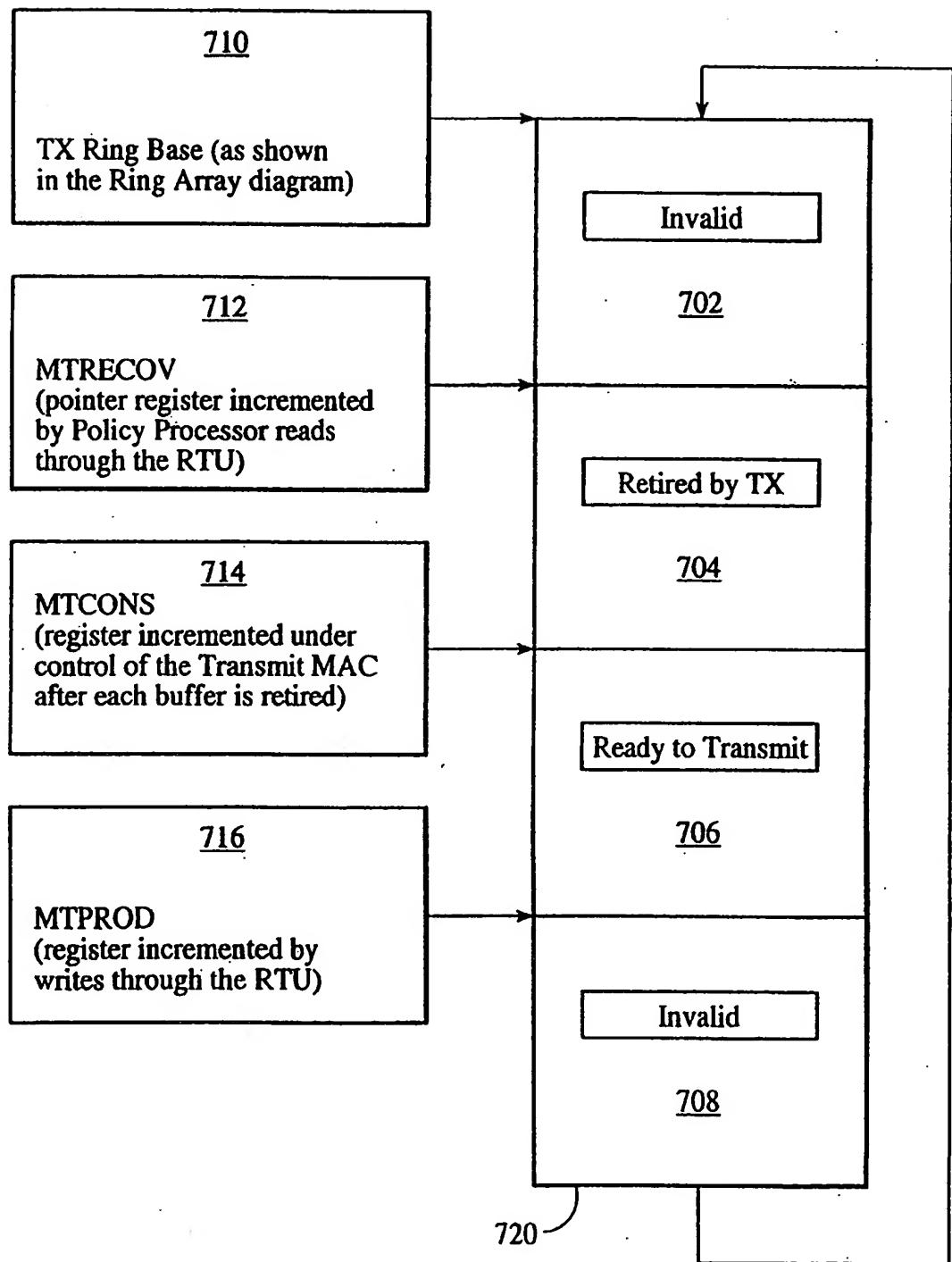


FIG. 8

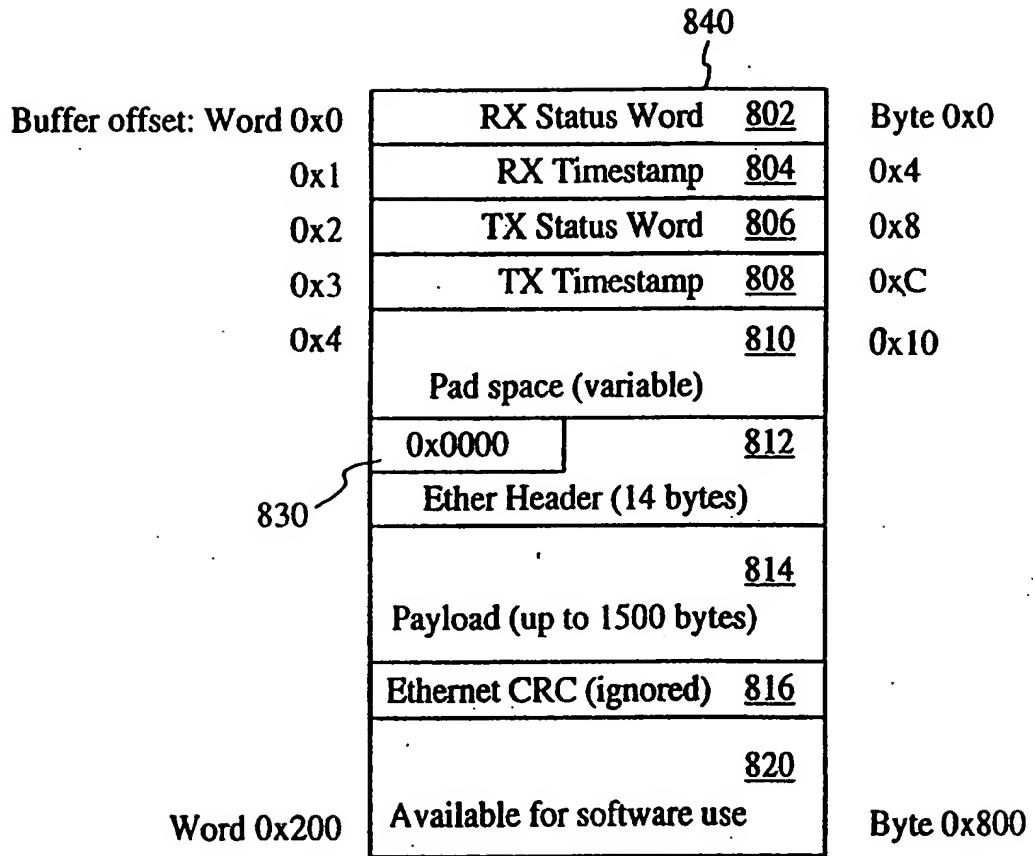


FIG. 9

DO NOT
DISTURB

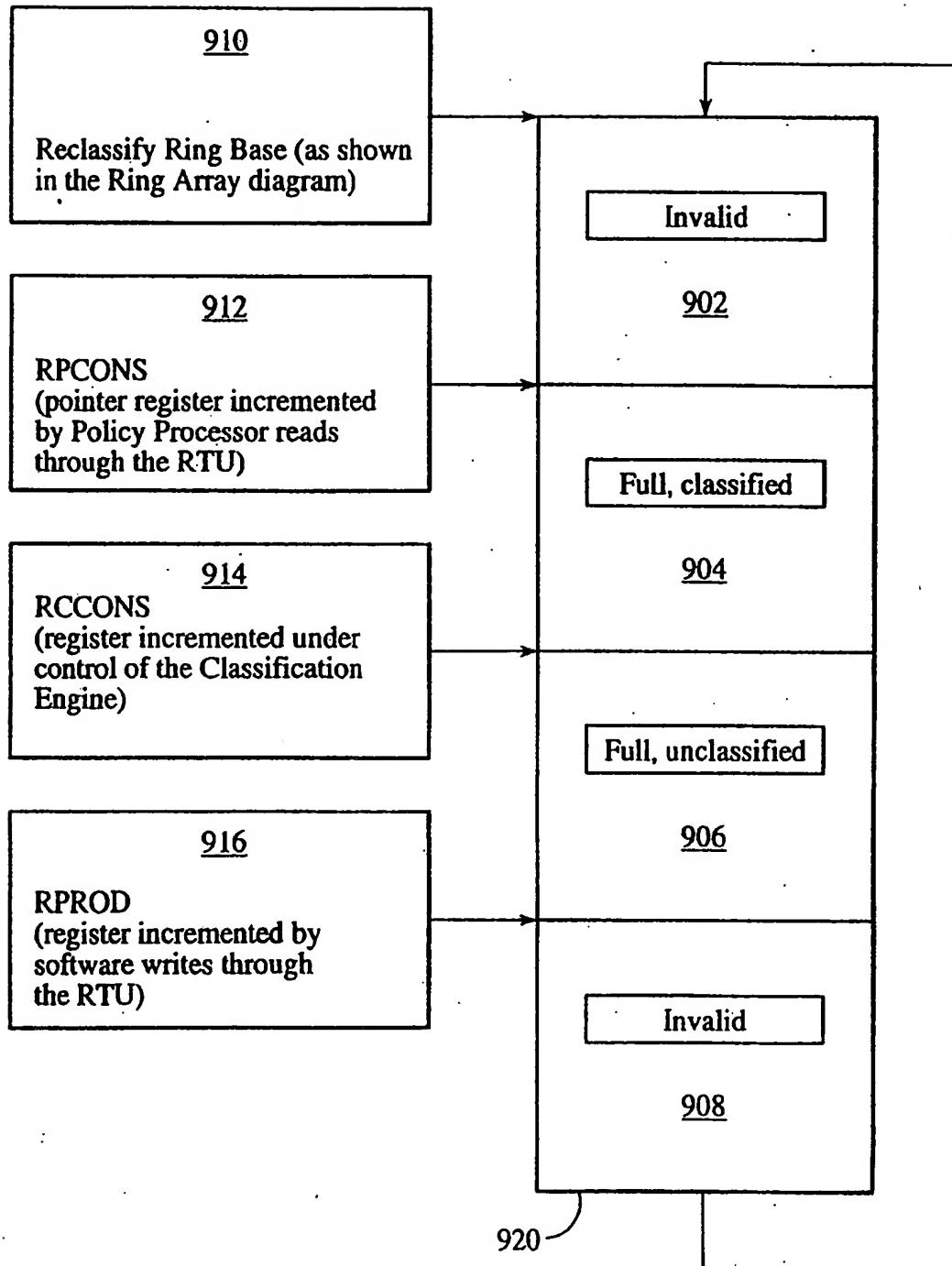


FIG. 10

U
D
B
P
T
S
U
I
O
N
C
N

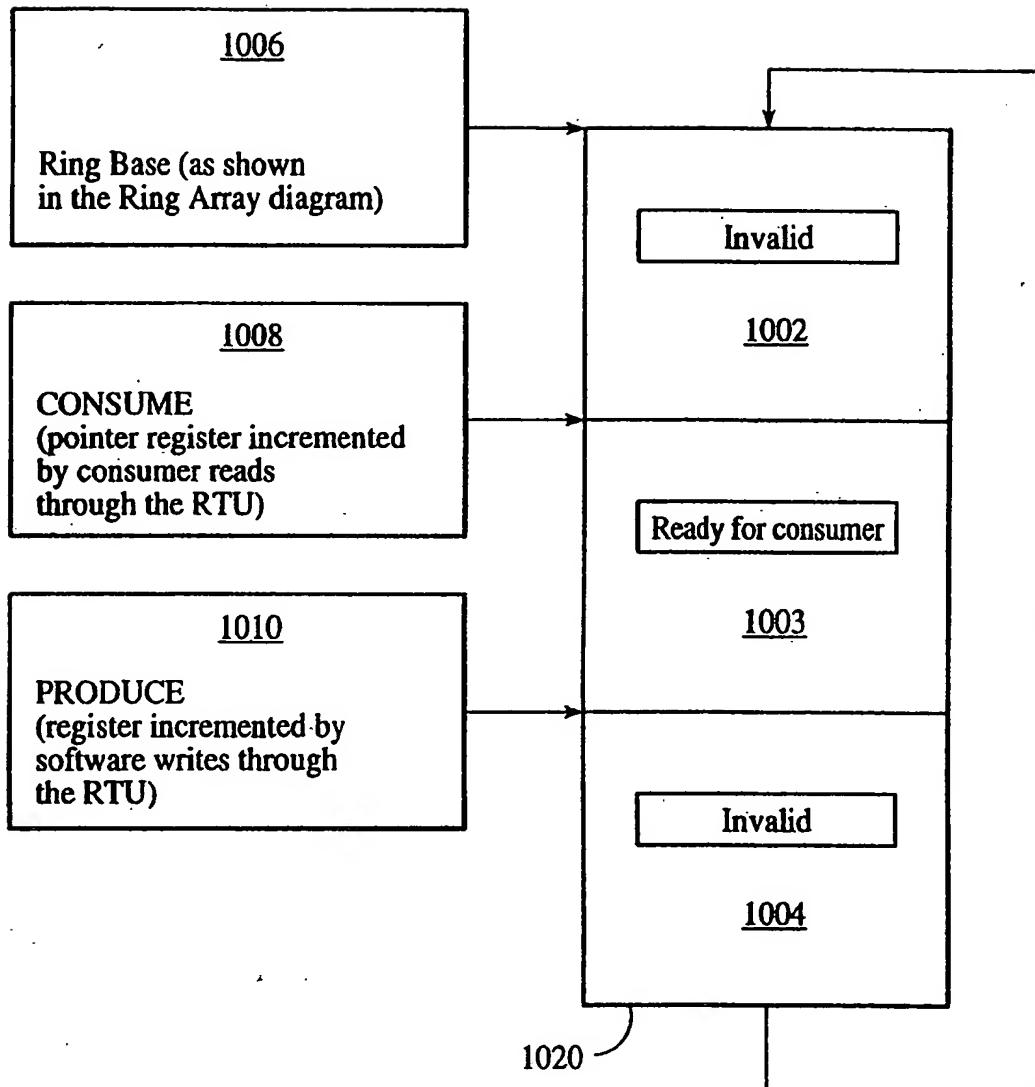


FIG. 11

1110
1112
1114
1116

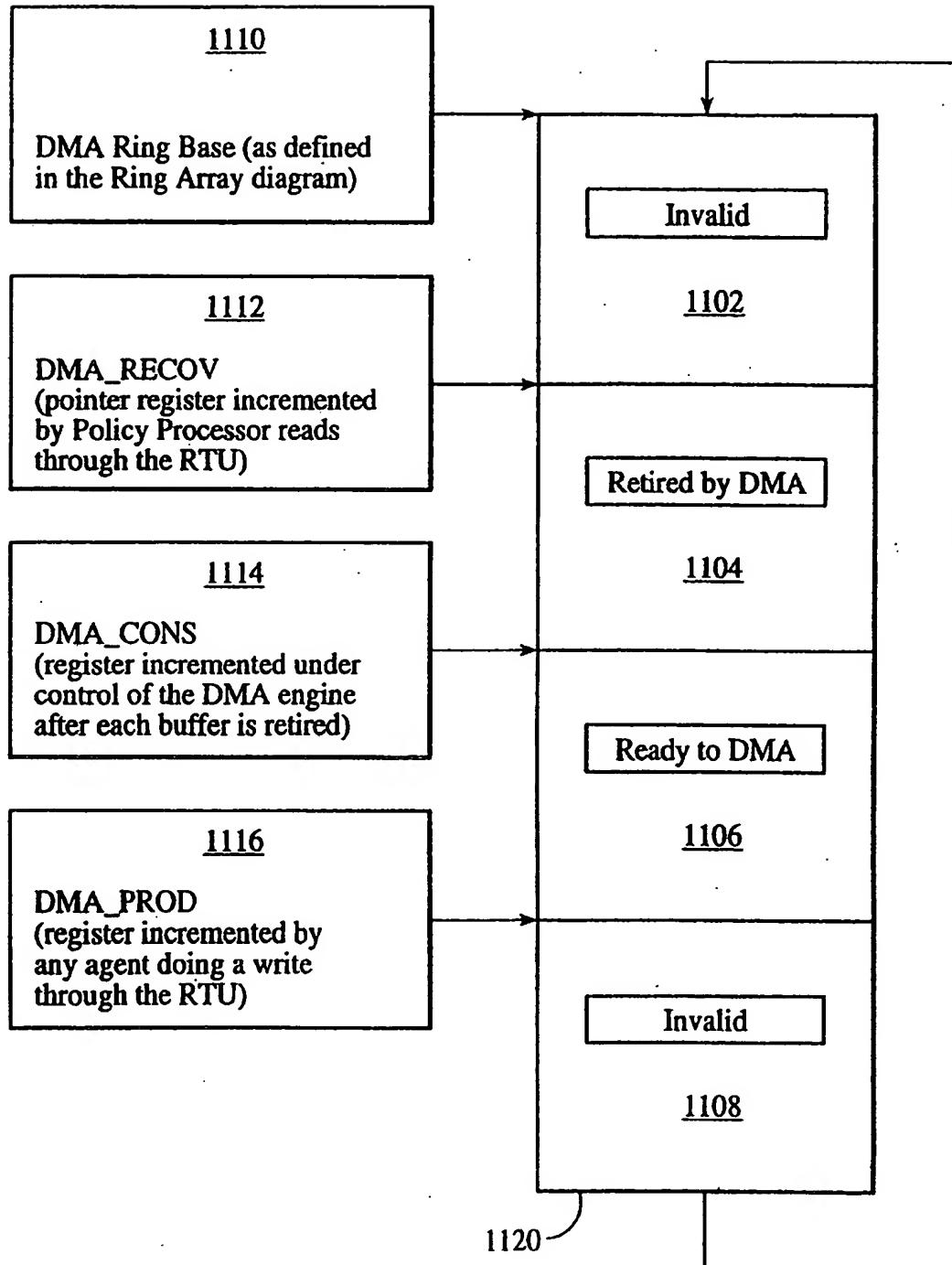
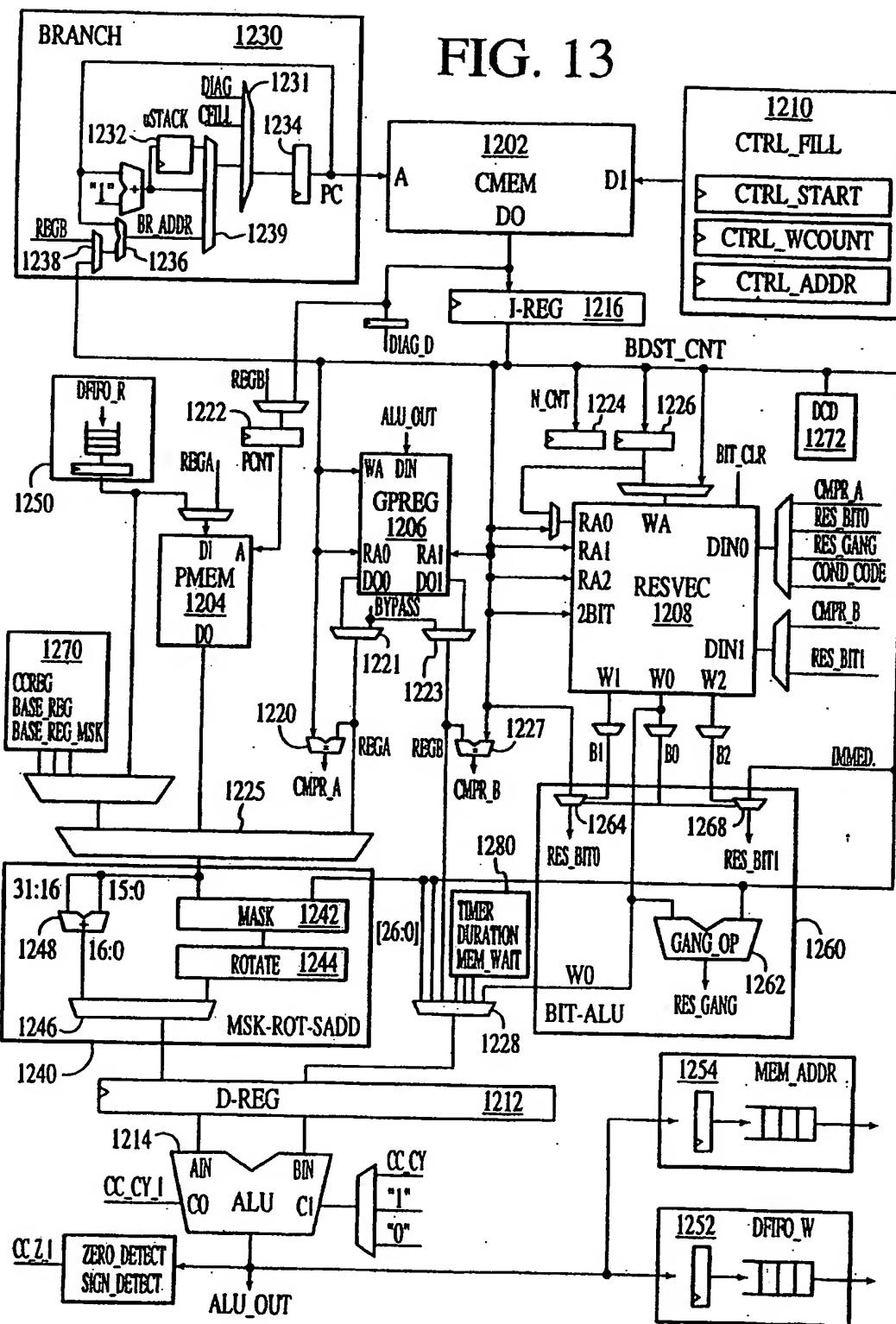


FIG. 12

FIG. 13



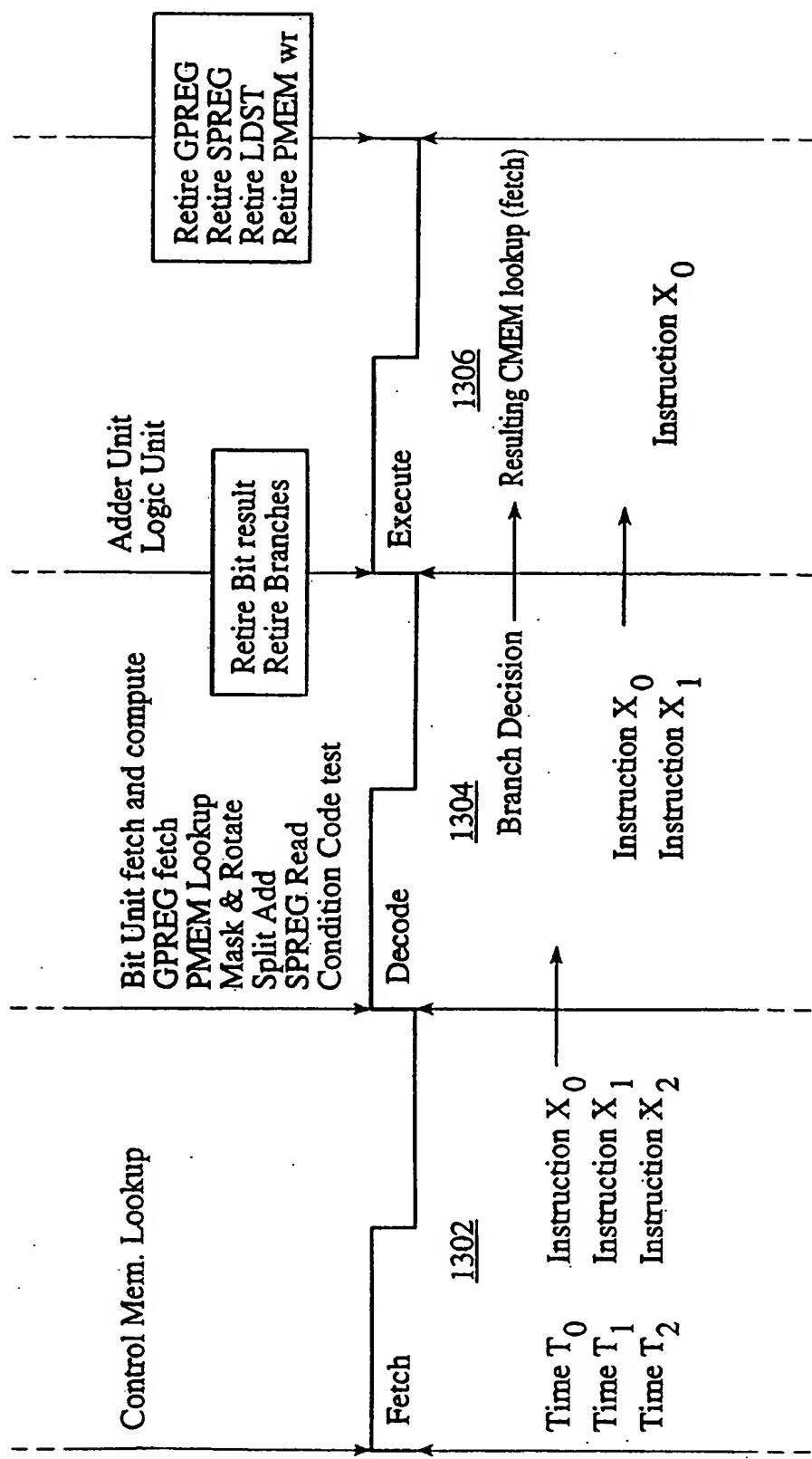


FIG. 14

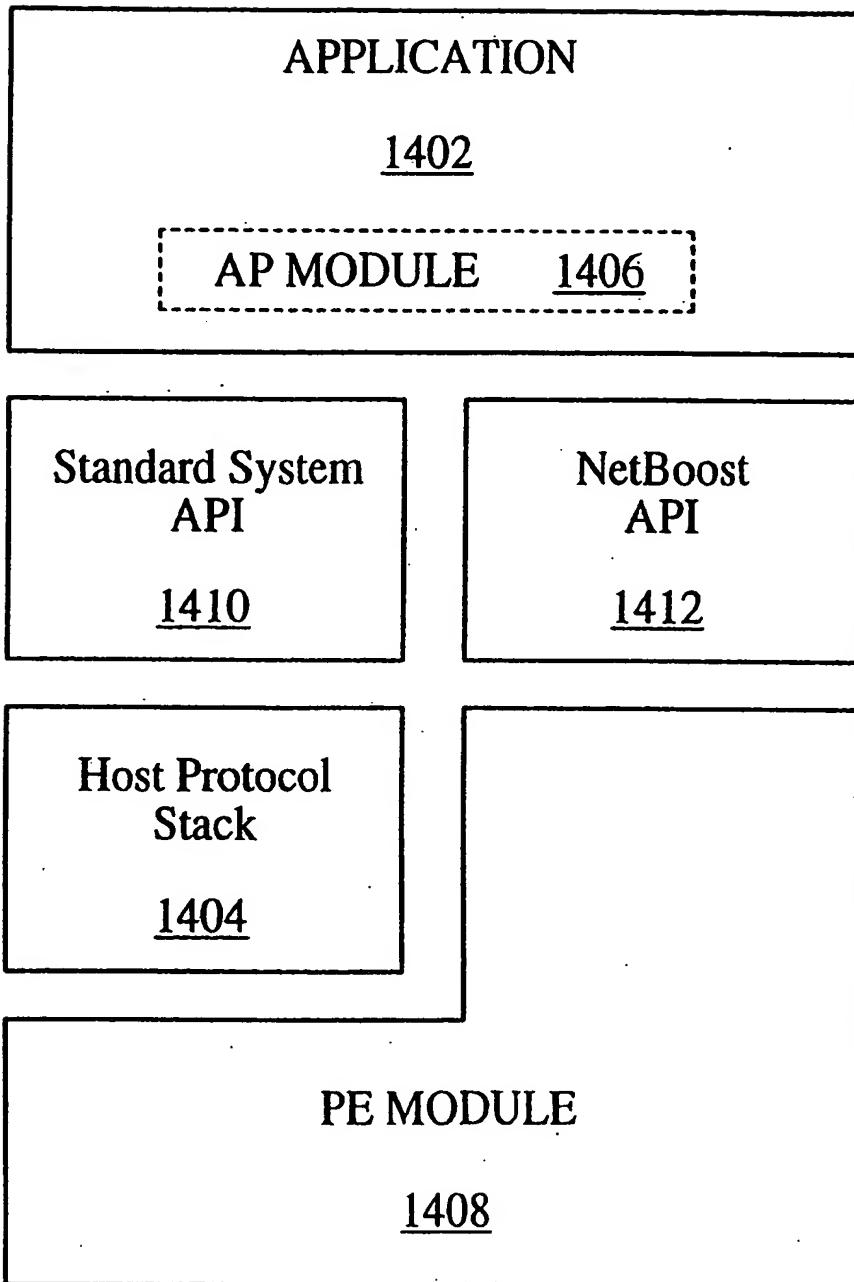


FIG. 15

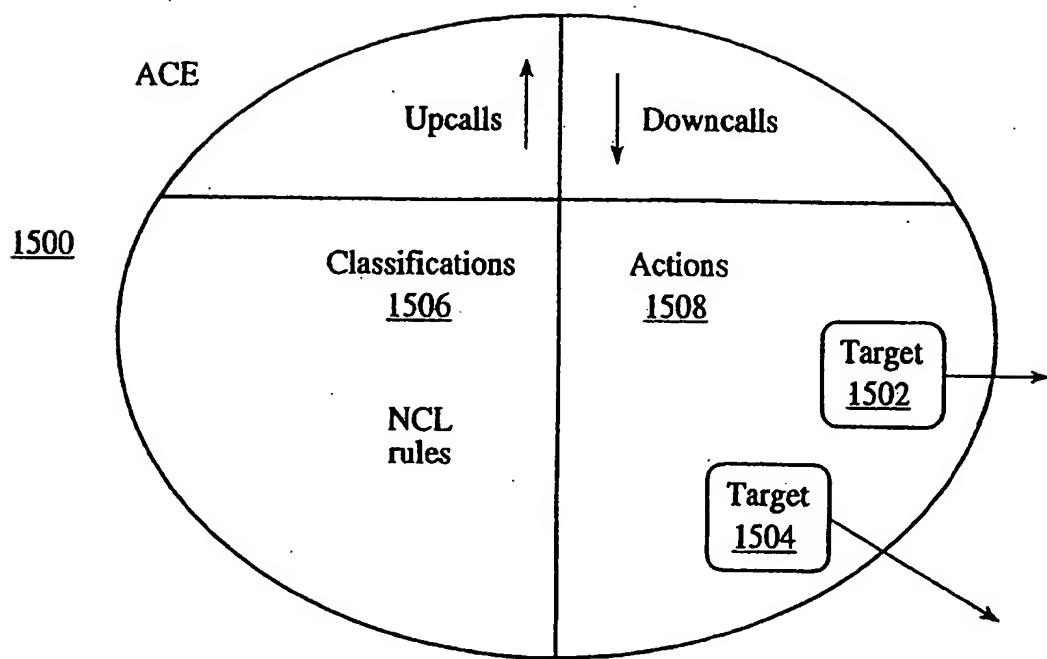


FIG. 16

2023-07-20

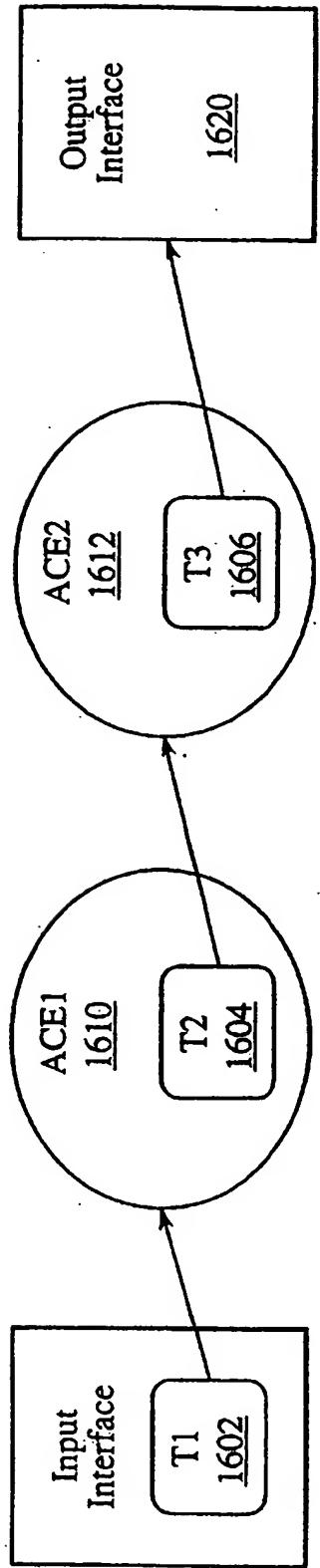


FIG. 17

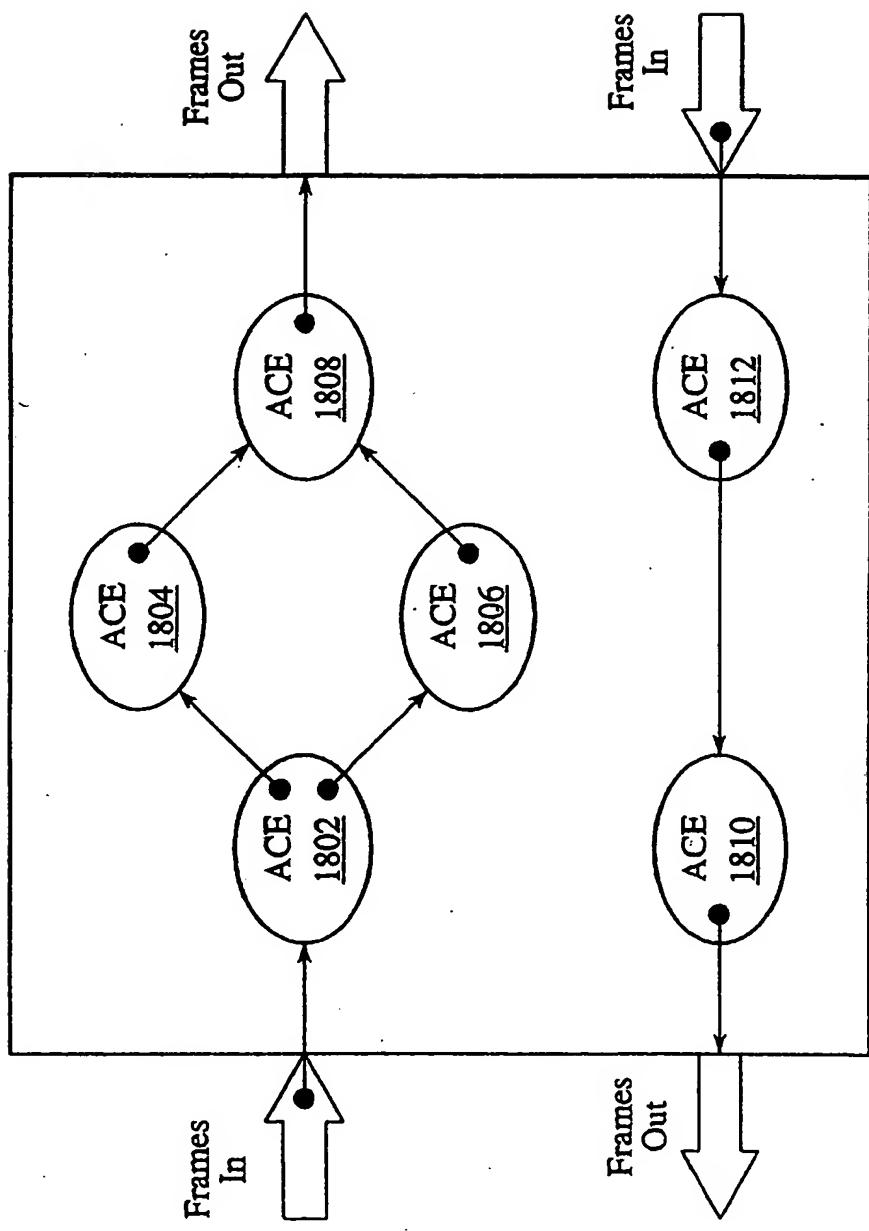


FIG. 18

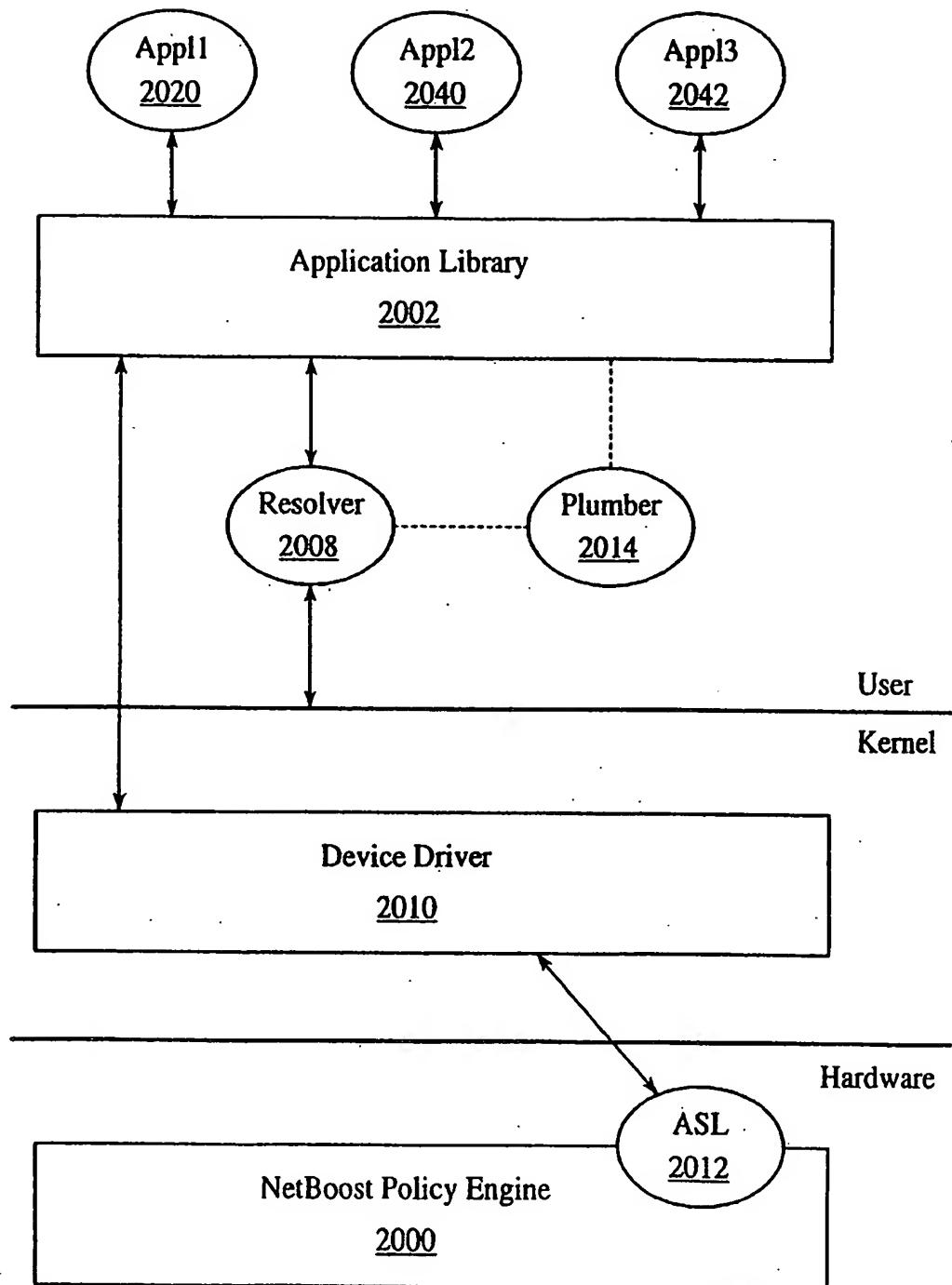


FIG. 19